

FIG.1

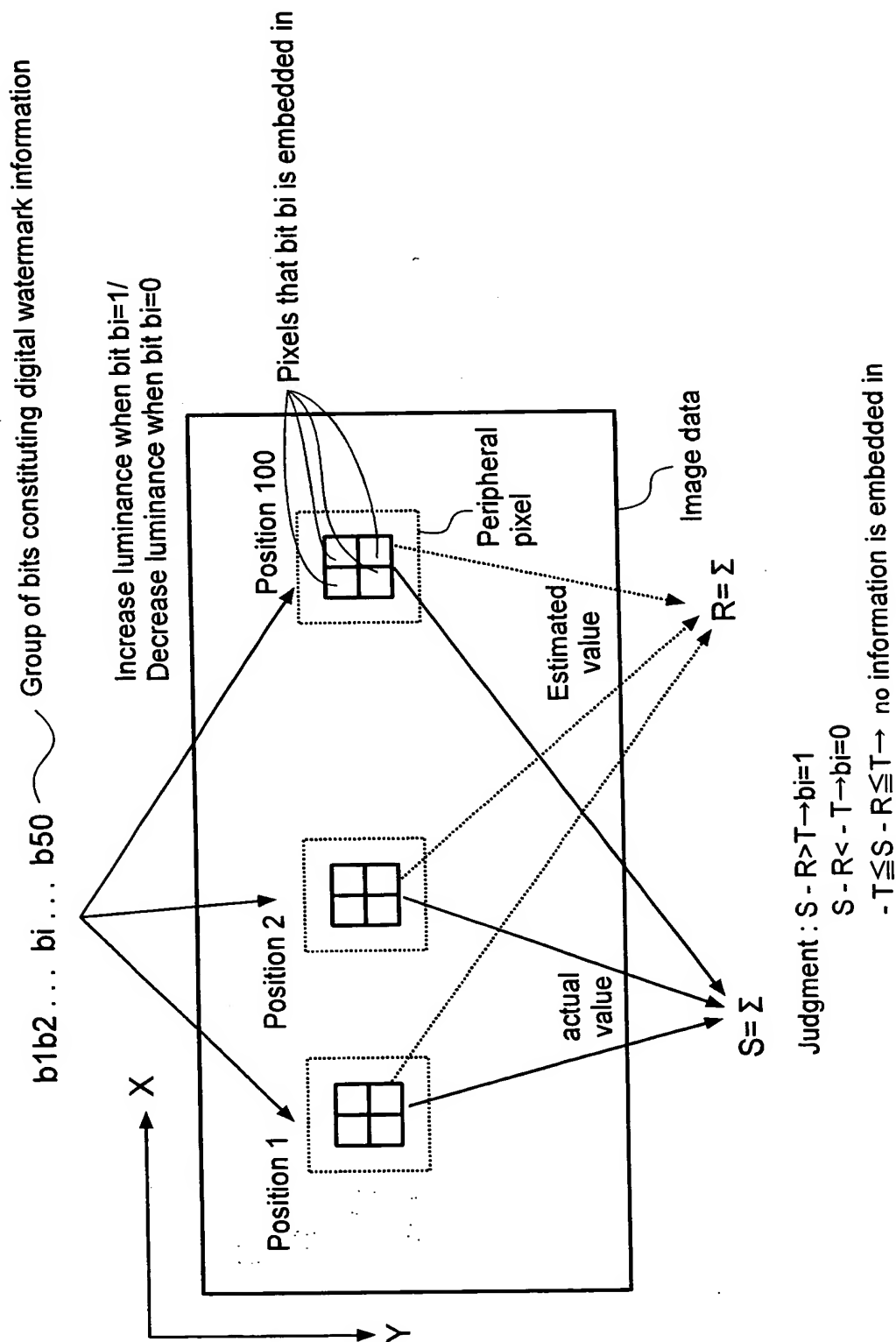


FIG.2

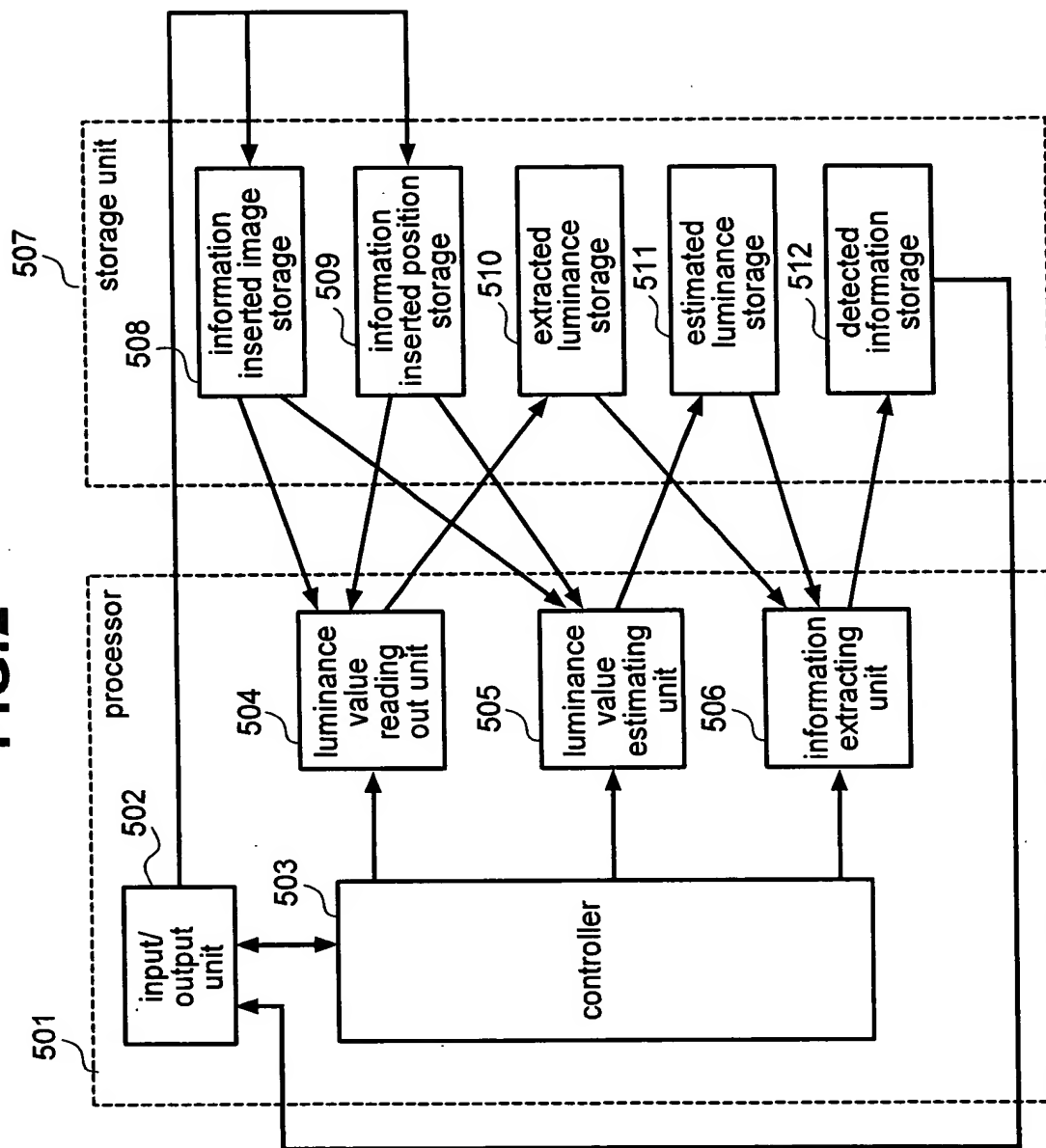


FIG.3

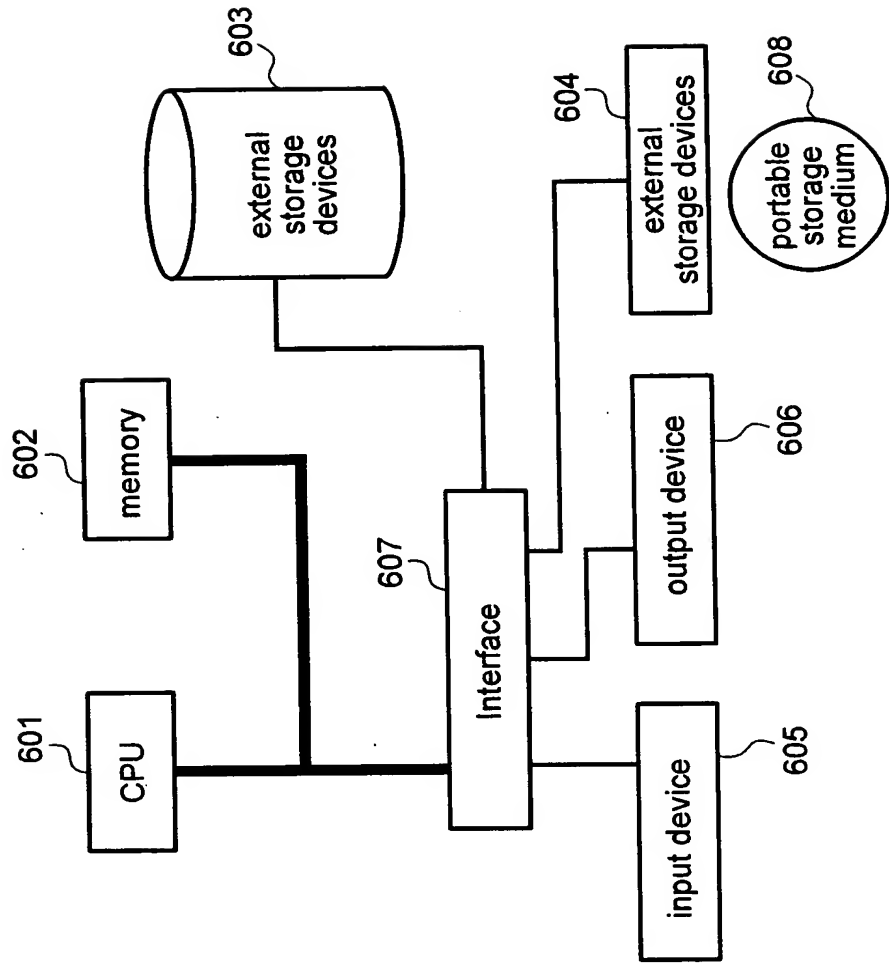


FIG.4

Positions Bit	Position 1	Position 2	...	Position j	...	Position 100
b1	P(1,1)	P(1,2)	...	P(1,j)	...	P(1,100)
b2	P(2,1)	P(2,2)	...	P(2,j)	...	P(2,100)
.
.						
.						
bi	P(i,1)	P(i,2)	...	P(i,j)	...	P(i,100)
.
.						
.						
b50	P(50,1)	P(50,2)	...	P(50,j)	...	P(50,100)

FIG.5

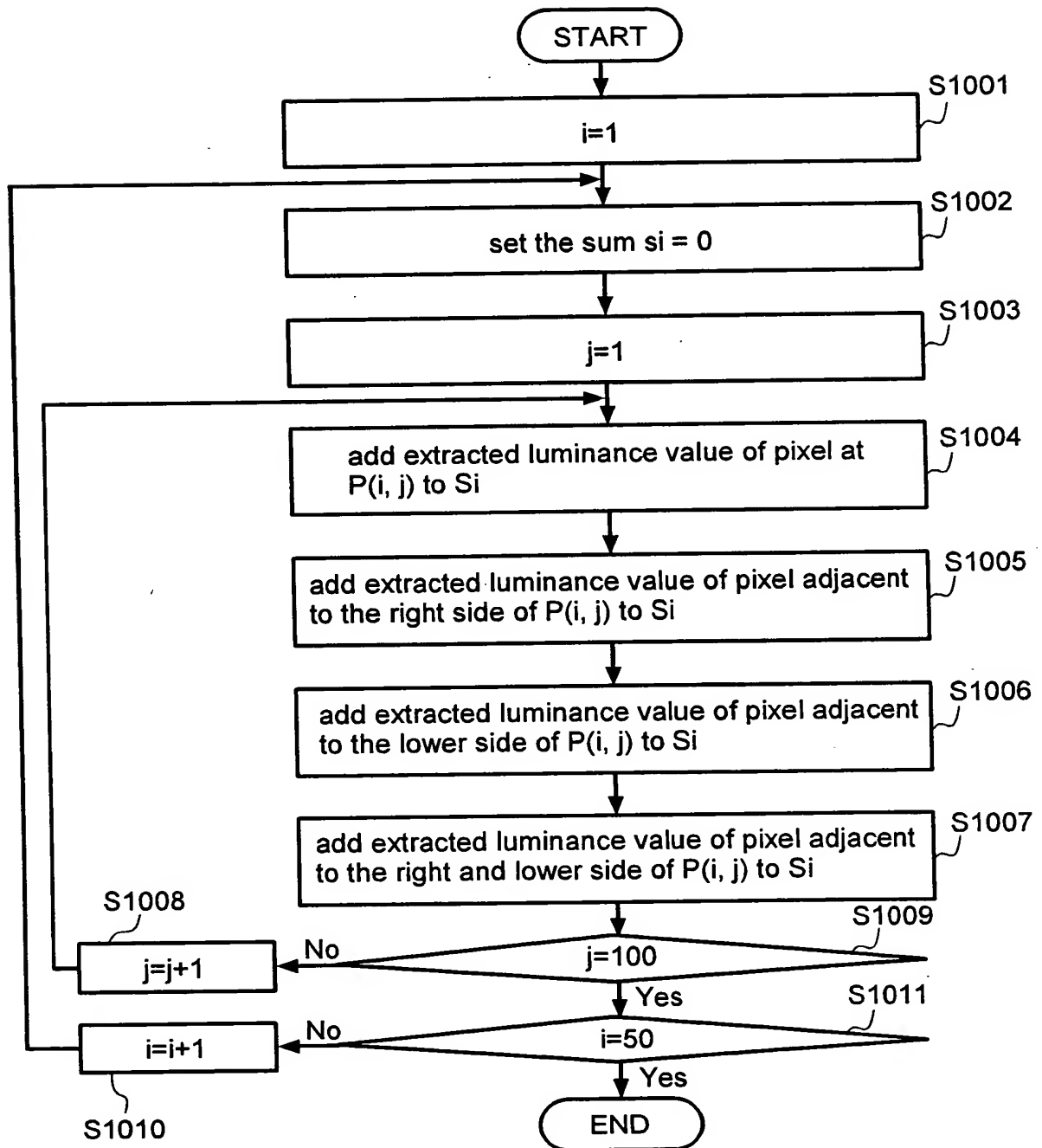


FIG.6

Bit	Extracted (and actual) luminance value
b1	S1
b2	S2
...	...
b _i	S _i
...	...
b50	S50

FIG.7

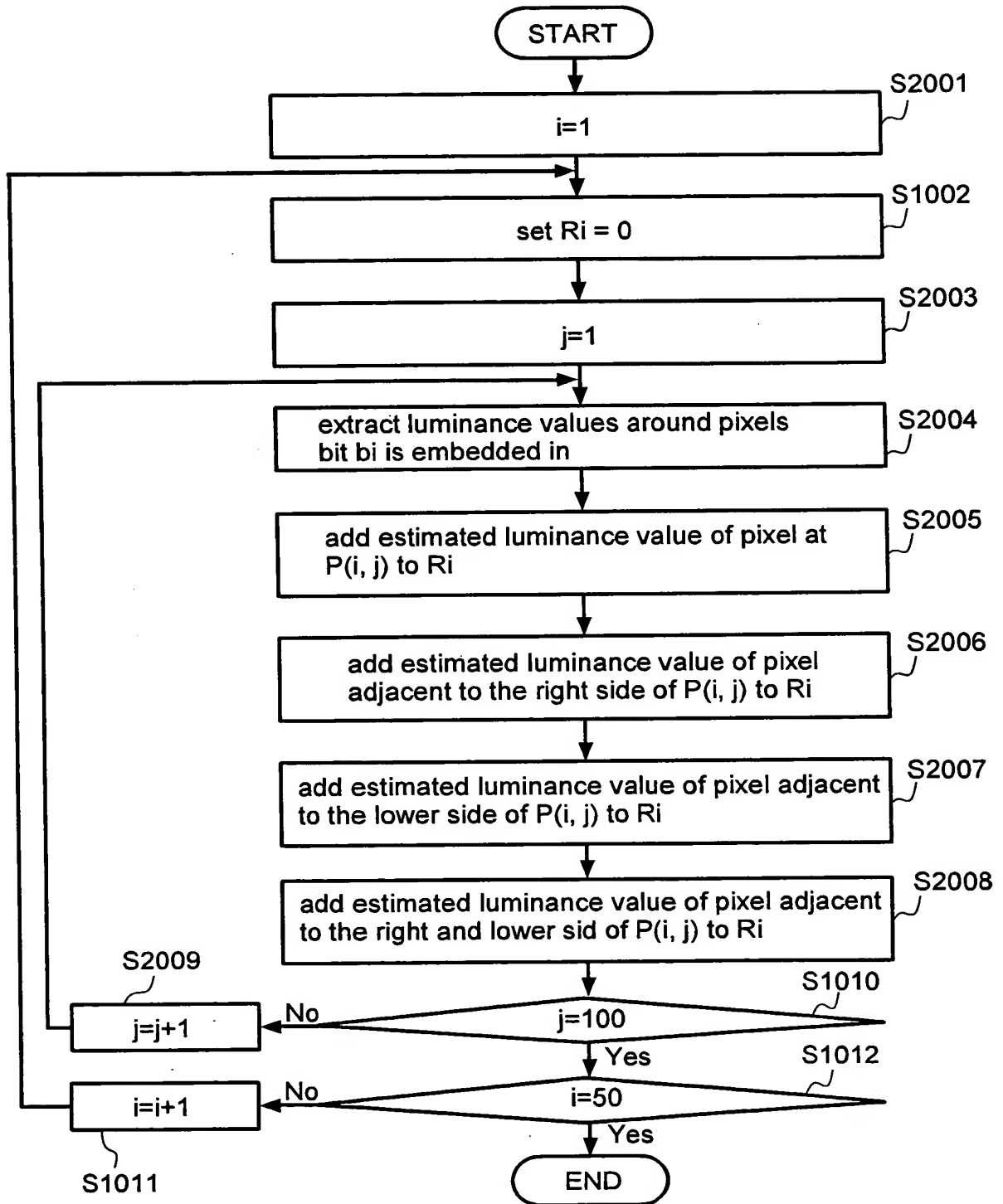


FIG.8

Bit	Estimated luminance value
1	B1
2	B2
...	...
i	B _i
...	...
50	B50

FIG.9

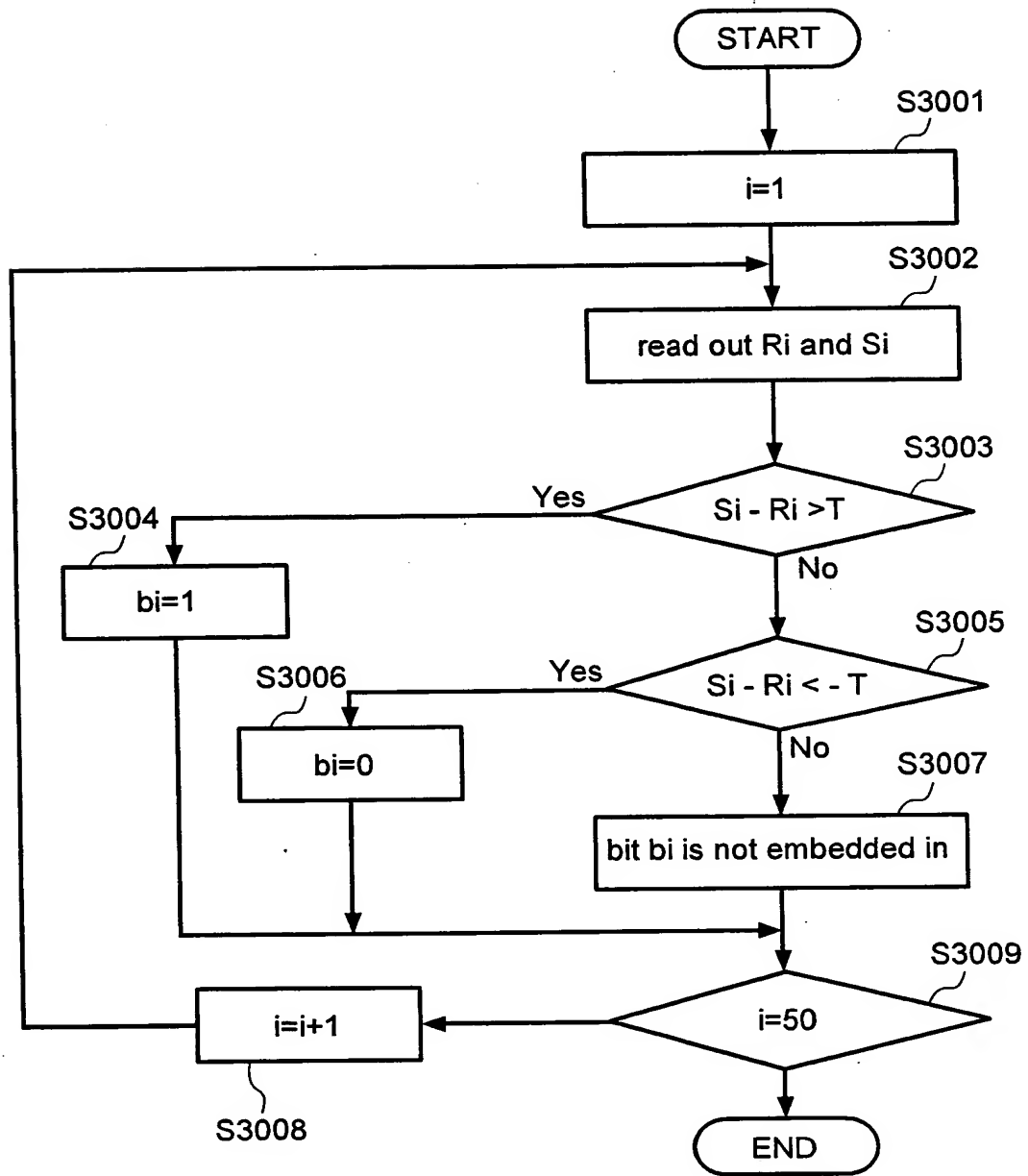


FIG.10

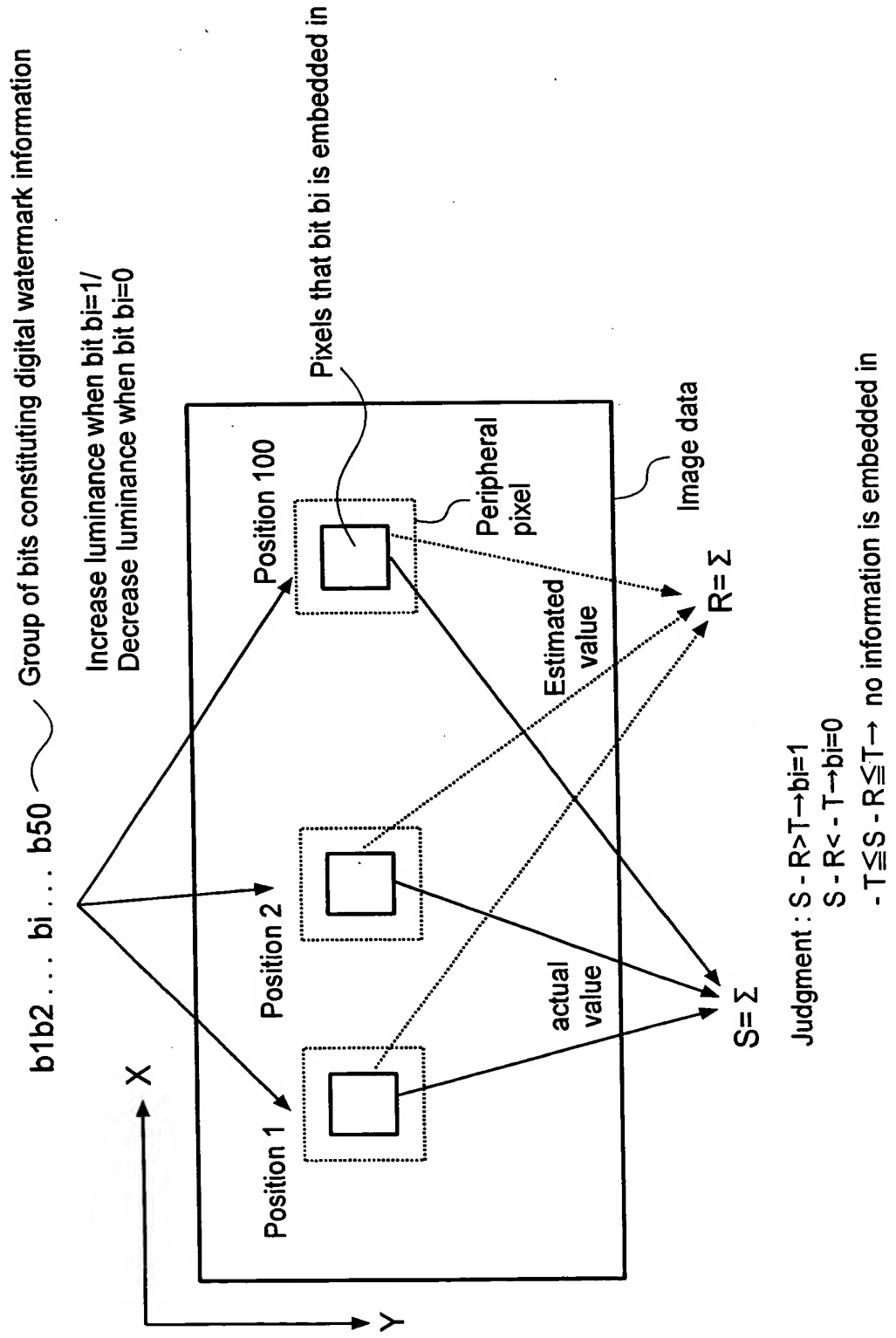


FIG.11

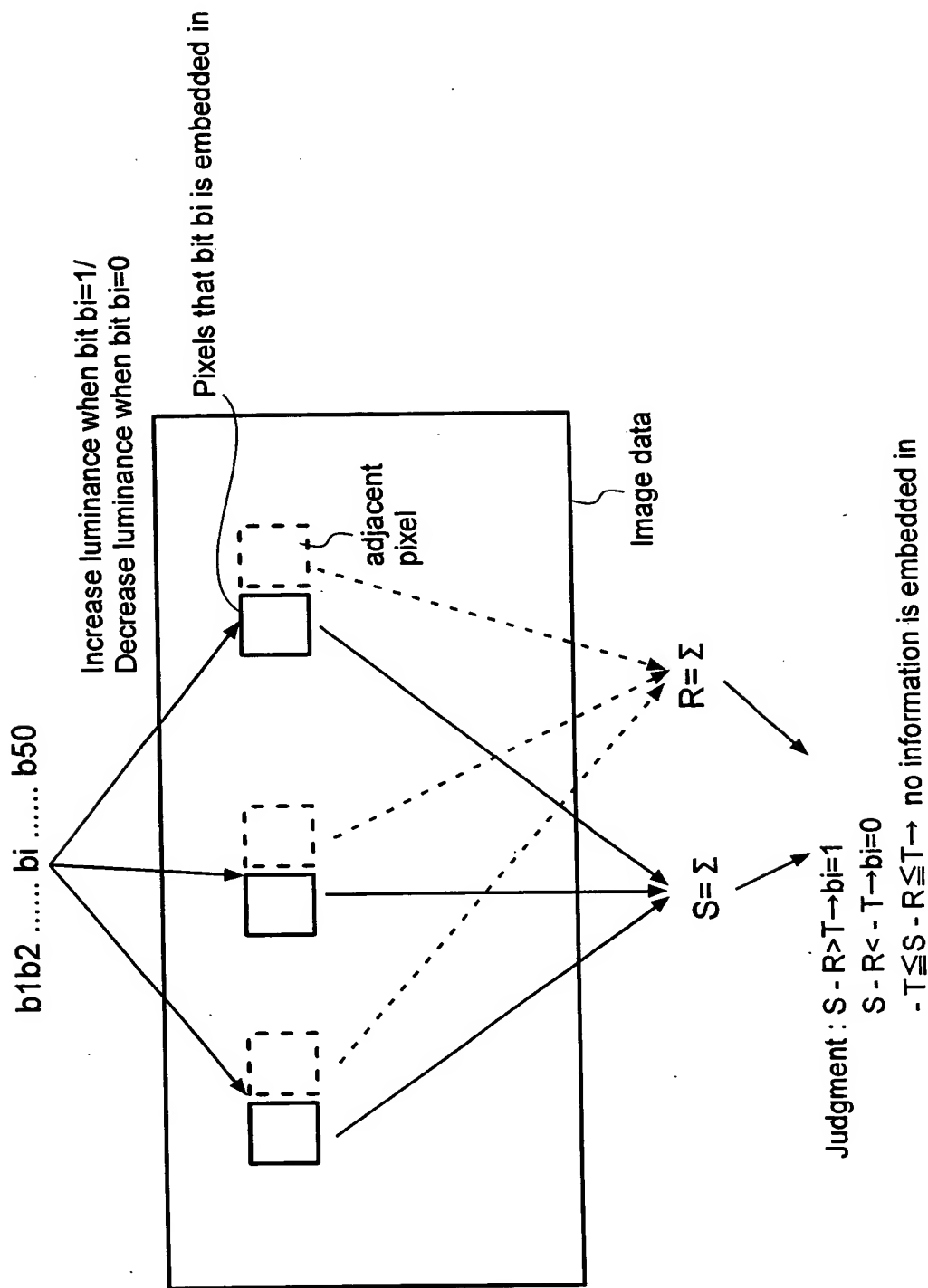


FIG.12

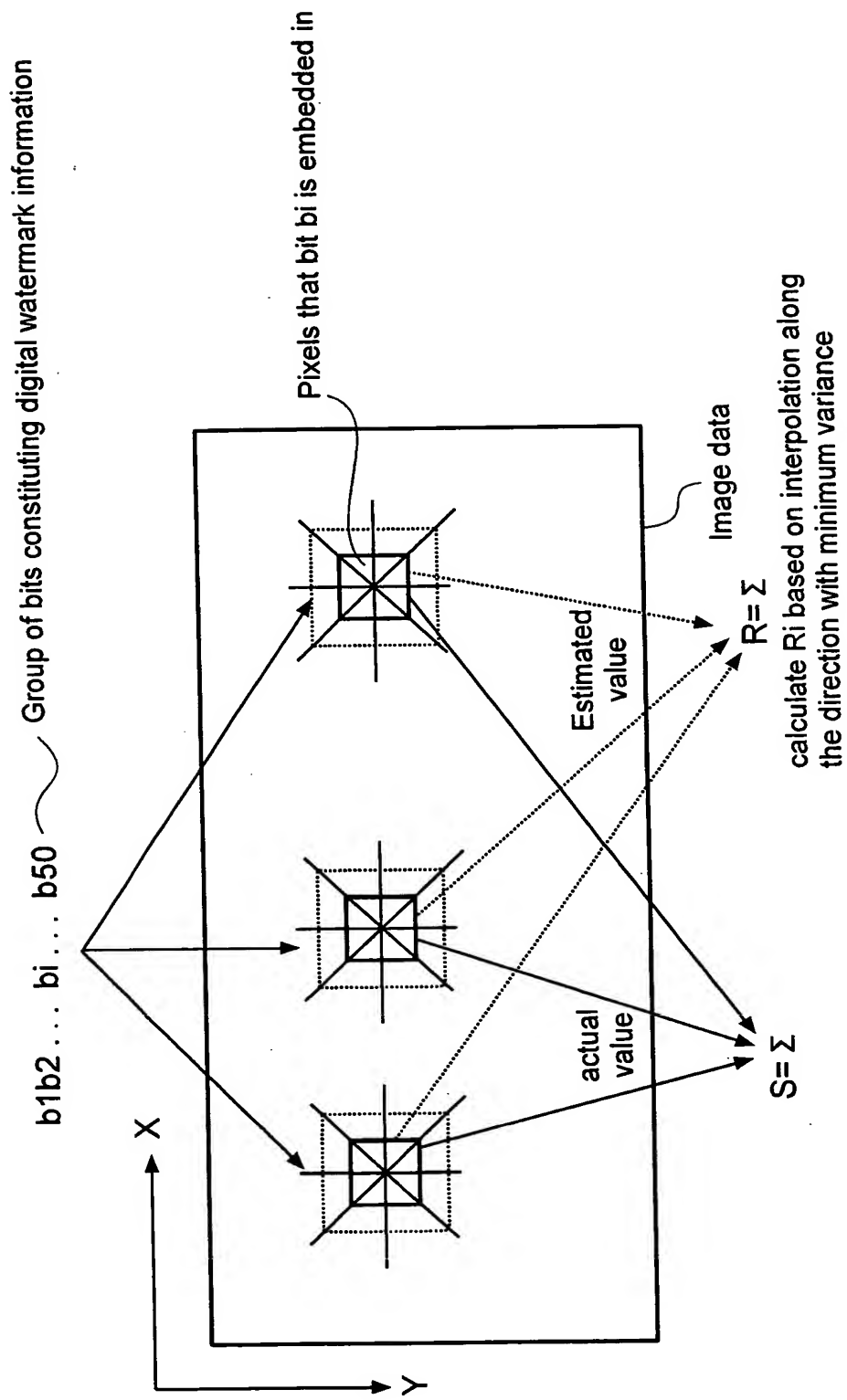


FIG.13

